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Cordially invite you to a webinar on

Indigenous R&D in Cyber Security: Initiatives in India

Dr. N. Sarat Chandra Babu

Executive Director
Society for Electronic Transactions and Security (SETS)

Date: Saturday, 7ᵗʰ November 2020 :: Time: 6 pm to 7 pm IST

Pl. register at https://bit.ly/3jXq6Bp

Dr. P Sakthivel	Mr. HR Mohan	Mr. PV Subramanian	Dr. N Sarat Chandra Babu
Chair, IEEE CS Madras	Chair, ACM Chennai	Chair, CSI Chennai	Executive Director, SETS

About the Presentation

The Digital India initiative has seen innovative use of technology in different sectors of economy. This has become a blessing in disguise during the present pandemic time. The entire country has the audacity to prove that it can keep moving digital way and keep performing in the sectors of health, education, banking, e-commerce, transportation etc.

The prime focus of 'Digital India initiative' is to empower the citizens with e-access to government services. The initiative has three core components: digital infrastructure, digital services and digital literacy. So, with the growing requirements, the technology has evolved and still will evolve with new devices, techniques and tools. The internet has reached both urban and rural population. On the other hand, with the growing use of Internet, Smart Phones, IoT, and Cloud Computing technologies, the concern due to growing cyber-attacks at various levels has also drastically increased. Realizing the need to safe guard the people, enterprises and the nation from various cyber-attacks, emanated from with-in and outside the country, Government of India has taken several steps, including the indigenous R&D solutions/ products for cyber security.

This talk will give a snapshot of cyber security R&D ecosystem, various R&D initiatives and the concerted efforts put-in by various stakeholders (Central and State Governments; Industry; Research in Academic institutes and R&D labs) in our country.

The talk also covers the technology trends such as AI & Blockchain for Cyber security, Security by Design, Mobile Security, IoT Security, Hardware Security, Cyber security in quantum era. The importance of skilling the human resources with the requisite skills in different areas of cyber security is also emphasized.

About the Speaker

Dr. Sarat is presently working as the Executive Director of Society for Electronic Transactions & Security (SETS), Chennai. Dr. Sarat obtained his Ph.D. from IIT, Delhi. He has considerable experience of over three and half decades in R&D, Project implementation and Co-ordination, Education & training.   At SETS he is leading the cyber security teams working in Hardware Security, Cryptography (Post quantum & QKD), and network security. He initiated the AI for Cyber security Project management group at SETS and also a Walk-in Cyber Security Education & Research lab.

Prior to joining SETS, he worked as Executive Director, C-DAC Bangalore and he was Founder Director of C-DAC Hyderabad. He worked at Department of Electronics, Govt. of India (Presently MeitY) at various levels. He is responsible to initiate Canal automation projects for Irrigation. He contributed to first digital exchange while working at Indian Telephone Industries Ltd.(ITI), Bangalore. He worked as faculty at REC, Warangal (presently NIT, Warangal) and taught Microprocessors, Analog/ Digital ICs etc.

Dr. Sarat is responsible for new R&D initiatives in the areas of Cyber security, Ubiquitous computing, IOT for Smart Cities, System software in National Super Computing Mission (NSM) etc. He has lead the team of experts in arriving at the Feasibility study report of India Microprocessor. He led the R&D teams working in the Cyber security in the areas of end systems security, Network security, SCADA security, Cloud security, Cryptanalysis, etc while working at C-DAC-Hyderabad and Bangalore. He designed a six months diploma in Embedded Systems Design, now popular as DESD at C-DAC. Responsible for conducting many National conferences, Workshops such as Think parallel; ParCompTech; ElelTech; WORTICS etc. He has published 72 papers in various National and International Journals & Conferences. He is Senior member of IEEE, Member-ACM, CSI and Fellow IETE.

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