Strategy and Synergy for Security

SOCIETY FOR ELECTRONIC TRANSACTIONS AND SECURITY [SETS]
CIT Campus, MGR Knowledge City, Taramani, Chennai–600113, India

Advertisement No. SETS/Chn/Rec/Proj/2025-26/49 Date: 13th February 2026

Society for Electronic Transactions and Security [SETS] is a Society under the Societies Registration Act, XXI of 1860, dedicated to carry-out Research and Development in the field of Information Security focusing on the key verticals, namely, Cryptology and Computing, Hardware Security, Quantum Security and Network Security.

SETS invites applications from citizens of India to fill up the following positions for a Research & Development project in the area of Cybersecurity:

The descriptions of position, detailed qualification requirements and salary are given below:

Name of Post	Project Associate (Software) Post Code: 26-PA-02
Total Number of Posts	FOUR
Age Limit	Not more than 35 years as of closing date of advertisement
Experience	0 - 4 Years of Relevant Experience
Essential Qualification	B. Tech /B. E in Computer Science / Information Technology/ Information Security/ Cyber Security / ECE or equivalent with 60% or above marks or equivalent
Desirable Qualification	M.Tech / M.E in Computer Science / Information Technology/ Information Security/ Cyber Security or equivalent with 60% or above marks or equivalent
Areas of Skill sets/ Knowledge required	a) Programming Experience in C/C++, Python and MATLAB. b) Experience with networking protocols: deep understanding of TCP/IP, DNSDHCP, HTTP/HTTPS, etc. c) Knowledge of Firewalls, VPNs, IDS/IPS, SIEM and SoC tools d) Packet Analysis Tools: Familiarity with tools like Wireshark, tcpdump, and NetFlow for deep packet inspection and troubleshooting. e) Experience in Software testing (manual and/or automation). f) Hands-on Experience in Penetration Testing Tools, Web Application Security and Linux Hardening. Knowledge on RTOS based firmware. g) Experience in Exploit Development, code reverse engineering., binary analysis tools (E.g. Ghidra, IDA Pro), Assembly language for ARM, MIPS and x86 architectures. h) Problem-Solving, Analytical Thinking capabilities.
Remuneration	Consolidated salary would be Rs. 40,000/- to 60,000/- per month based on the higher qualification and relevant experience

The positions of **Project Associate** as proposed is purely temporary and would be filled on a contract basis with a consolidated salary under project mode. The duration of the assignment is initially for a period of One Year and would be extended further based on the need of the project and performance. There is no scope of continuation / regularization/ absorption under any circumstances.

Application Procedure:

1. The candidate is requested to attach the following documents: -
   a) Scanned copies of academic certificates (Single PDF document)
   b) Scanned copies of Experience certificates (Single PDF document)
2. The last date for uploading applications is 19.02.2026 before 6 PM.
3. Shortlisted candidates would be required to attend a Written Test and / or Interview at SETS, Chennai. The date and time for this would be intimated to shortlisted candidates by E-mail.

Terms and Conditions:

1. The shortlisted candidates would be required to bring all their original testimonials for verification on the interview day.
2. No TA/DA will be given to candidates appearing for interview.
3. The prescribed qualifications are minimum and mere possession of the same does not entitle the candidate to be called for the written test or interview. The decision of the Executive Director of SETS in all matters relating to eligibility, acceptance or rejection of the applications, cancellation of advertisement and filling up or not filling up of post will be final and no inquiry or correspondence will be entertained in this matter.
4. Number of vacancies may increase/decrease depending upon SETS requirements and such changes will be made by SETS without any notice.

Executive Director