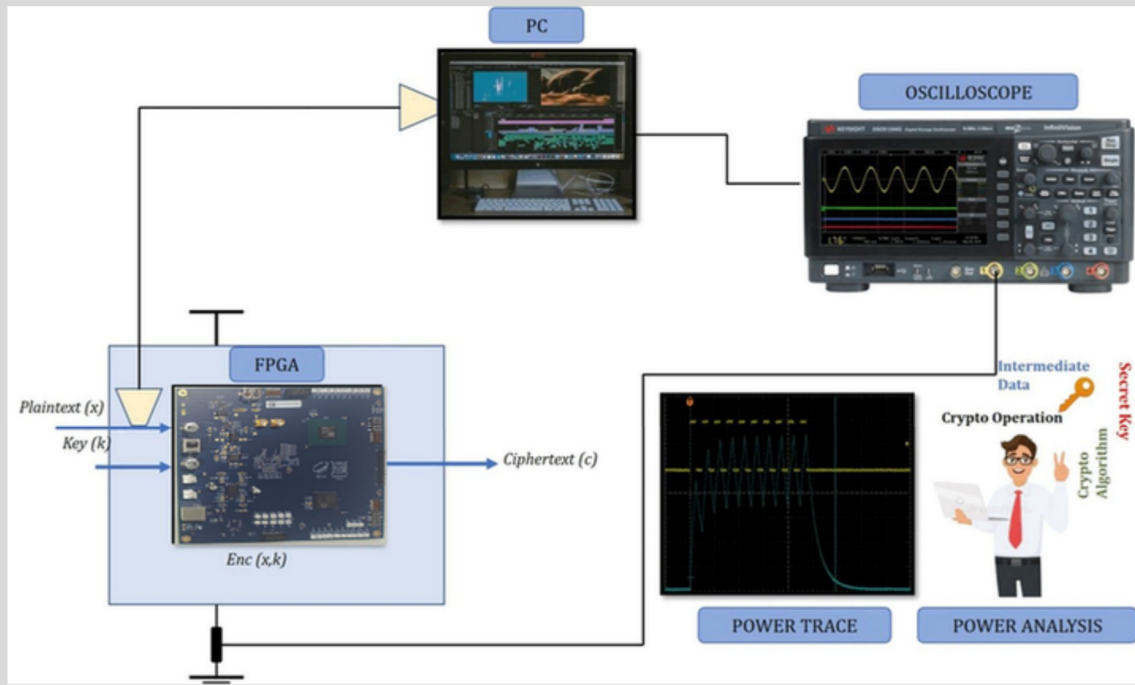




ANVAYAM

SETS-ANVAYAM (Analysis of side-channel VulnerAbilities for securitY AssessMent) is a standalone FPGA board for side-channel evaluation developed by SETS. It is a specialized hardware platform designed to measure physical leakages, such as computation time, power consumption and electromagnetic emissions, from FPGA device during cryptographic operations.

SETS has also developed SETS-THADAM (Trace-based Hardware Analysis and Detection of cryptographic leakages through Automated Measurement), the analysis tool, which facilitates capture, evaluation, security validation and research in hardware-based cryptographic systems. SETS-ANVAYAM along with the SETS-THADAM can be used to perform compliance testing as per ISO/IEC 17825, a testing method for the mitigation of non-invasive attack classes against cryptographic modules.



ANVAYAM FPGA based Power/EM Measurement Hardware



THADAM Automation Software Tool

Beneficiaries



ISO/IEC 17825 Testing Laboratories



Hardware Security Research Teams



Academia and Researchers



Cryptographic Hardware Developers

