



## **SOCIETY FOR ELECTRONIC TRANSACTIONS AND SECURITY [SETS]**

CIT Campus, MGR Knowledge City, Taramani, Chennai – 600 113, India.

**Advertisement No. SETS/Chn/Rec/JRF/2026-27/52 - Date: 29th May 2026**

**Society for Electronic Transactions and Security [SETS]** is a Society under Societies Registration Act, XXI of 1860, dedicated to carry-out Research and Development in the field of Information Security focusing on the key verticals, namely, Cryptology and Computing, Hardware Security, Quantum Security and Network Security.

SETS invites applications from citizens of India for filling up the following positions for a Research & Development project in the area of Cybersecurity.:

### **1. Junior Research Fellow (JRF) – 2 Positions**

The descriptions of positions, detailed qualification requirements and Fellowship are given below:

#### **Junior Research Fellow (JRF)**

| <b>Name of Post</b>            | <b>Junior Research Fellow (JRF)</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
|--------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Total Number of Posts</b>   | Two                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>Age Limit</b>               | Not more than 28 years 31.12.2026                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>Essential Qualification</b> | First Class M. Tech /M. E / MS by Research (Embedded Systems/ Applied Electronics Communication Systems/VLSI Design/ Information Technology/ Electronic Engineering /Wireless Communication/ Computer Science/ Information Security/Cyber security/Network Security), MSc Applied Mathematics/Computer Science.<br><br>Copy of Valid GATE or CSIR–UGC NET certificate or National Level Examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc., |
| <b>Desirable skills</b>        | Candidates' expertise in Cyber security/Information Security/ Cryptography Hardware security/Quantum Security/Network Security/AI & Data science and Scientific writing skills.                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>Areas of Interest</b>       | Cryptology & Computing Security/Hardware Security/ Quantum Security/ Network Security                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| <b>Fellowship</b>              | As per DST Norms                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |

The duration of the Fellowship is initially for a period of Two Years and would be extended further based on the need of the project and performance. There is no scope of continuation/regularization/absorption under any circumstances.

### **Application Procedure:**

- 1. Only applications received through ONLINE- [www.setsindia.in/careers](https://www.setsindia.in/careers) will be considered. Submission of online applications will open at 10:00 AM on 29.05.2026**
- 2. The candidate is requested to attach the following documents: -**
  - a) Scanned copy of Personal Particulars form (pdf format) duly filled and signed. The Personal Particulars form is available at SETS website <https://setsindia.in/careers>.
  - b) Copy of Valid GATE or CSIR–UGC NET certificate or National Level Examinations
  - c) Scanned copies of certificates of Academic Qualifications (Single PDF document)
  - d) Scanned copies of Experience certificates (Single PDF document)
- 3. You may upload applications via online application around the year. However, the applications will be considered as and when requirement/vacancy arises.**
- 4. Shortlisted candidates would be required to attend a Written Test and/or Interview at SETS, Chennai. The date and time for this would be intimated to shortlisted candidates by email.**
- 5. The Selected candidates may be considered to pursue Ph.D. in the areas of SETS R&D including Cryptology & Computing Security/Hardware Security/ Quantum Security/ Network Security**
- 6. The following Mandate to be noted:**
  - a) To be full time available at SETS
  - b) Contribute to SETS R&D Projects/Activities
  - c) Research Publications if any while working at SETS should duly endorsed or acknowledged by SETS and along with SETS Scientists' In-charge.

### **Terms and Conditions:**

- a) The shortlisted candidates would be required to bring all their original testimonials for verification on the interview day.
- b) No TA/DA will be given to candidates appearing for interview.
- c) The prescribed qualifications are minimum and mere possession of the same does not entitle the candidate to be called for the written test or interview. The decision of the Executive Director of SETS in all matters relating to eligibility, acceptance or rejection of the applications, cancellation of advertisement and filling up or not filling up the post will be final and no inquiry or correspondence will be entertained in this matter.
- d) Number of vacancies may increase/decrease depending upon SETS requirements and such changes will be made by SETS without any notice.

**Executive Director**