



SOCIETY FOR ELECTRONIC TRANSACTIONS AND SECURITY [SETS]
CIT Campus, MGR Knowledge City, Taramani, Chennai-600113, India

Advertisement No. SETS/Chn/Rec/Proj/2026-27/69 Date: 2nd September 2026

Society for Electronic Transactions and Security [SETS] is a Society under the Societies Registration Act, XXI of 1860, dedicated to carry-out Research and Development in the field of Information Security focusing on the key verticals, namely, Cryptology and Computing, Hardware Security, Quantum Security and Network Security.

SETS invites applications from citizens of India to fill up the following positions for a Research & Development project in the area of Cybersecurity:

1. Project Associate – 2 Positions

The descriptions of position, detailed qualification requirements and salary are given below:

Name of Post	Project Associate	Post Code: 26-PA-20
Total Number of Posts	Two	
Age Limit	Not more than 35 years as on 15.09.2026	
Essential Qualification	M.Tech /M. E / MS by Research (Embedded Systems/Communication Systems/VLSI Design/ Electronic Engineering/Cyber security) or equivalent with 60% or above marks	
Experience	0-2 Years of relevant experience	
Remuneration	Consolidated salary would be Rs 50,000 to 60,000 per month, additional increment may be given based on the skill sets and relevant experience of candidate.	
Areas of Skill sets/ Knowledge required	• Hands-on experience in Xilinx Vivado tools, simulation using MATLAB / ModelSim and FPGA realization using VHDL/Verilog programming • Knowledge in FPGA debug tools such as ILA • Programming Experience in C/C++ and Python • Knowledge in Digital Electronics • Knowledge in Hardware Security/ Side Channel Analysis • Knowledge in PCB design and hardware design review • Knowledge in interfaces such as SPI, I²C, UART, PCIe or similar interfaces.	

The positions of **Project Associate** as proposed is purely temporary and would be filled on a contract basis with a consolidated salary under project mode. The duration of the assignment is initially for a period of One Year and would be extended further based on the need of the project and performance. There is no scope of continuation / regularization/ absorption under any circumstances.



Application Procedure:

1. Candidates are requested to provide the following details & Supporting documents: -
 - a) Scanned copy of Personal Particulars Form (pdf format) duly filled and signed. The Personal Particulars Form (PPF) is available at SETS homepage <https://setsindia.in/careers>
 - b) Scanned copies of academic certificates (Single PDF document)
 - c) Scanned copies of Experience certificates (Single PDF document)
2. Candidates are requested to attach the required documents, application with incomplete PPF form and without required certificates/documents are liable to be rejected.
3. The last date for applying for the posts is 15.09.2026 before 6:00 PM.
4. Shortlisted candidates would be required to attend a Written Test and / or Interview at SETS, Chennai.
5. The date and time for this would be intimated to shortlisted candidates by E-mail

Terms and Conditions:

1. The shortlisted candidates would be required to bring all their original testimonials for verification on the interview day.
2. No TA/DA will be given to candidates appearing for interview.
3. The prescribed qualifications are minimum and mere possession of the same does not entitle the candidate to be called for the written test or interview. The decision of the Executive Director of SETS in all matters relating to eligibility, acceptance or rejection of the applications, cancellation of advertisement and filling up or not filling up of post will be final and no inquiry or correspondence will be entertained in this matter.
4. Number of vacancies may increase/decrease depending upon SETS requirements and such changes will be made by SETS without any notice.

Executive Director